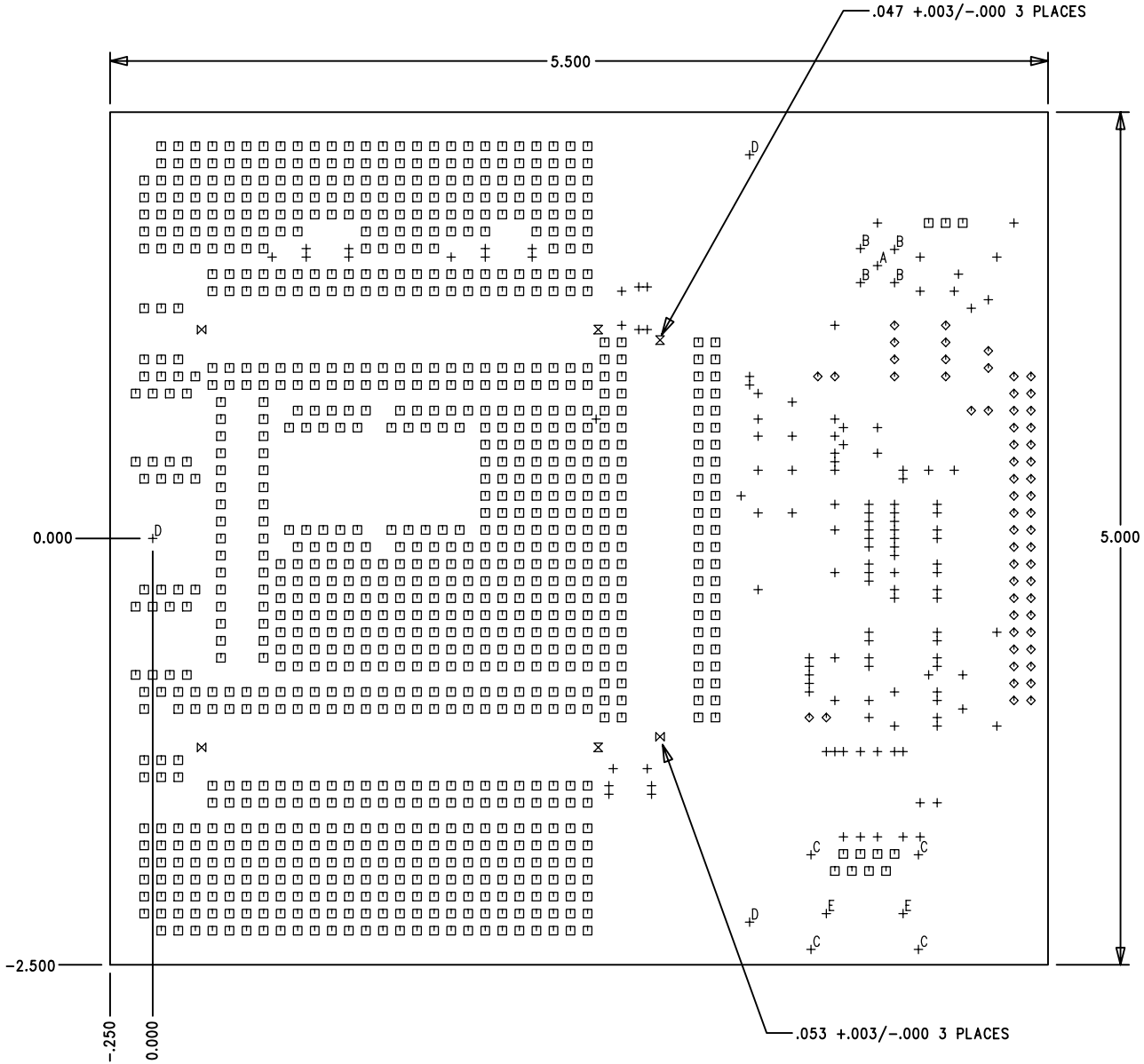


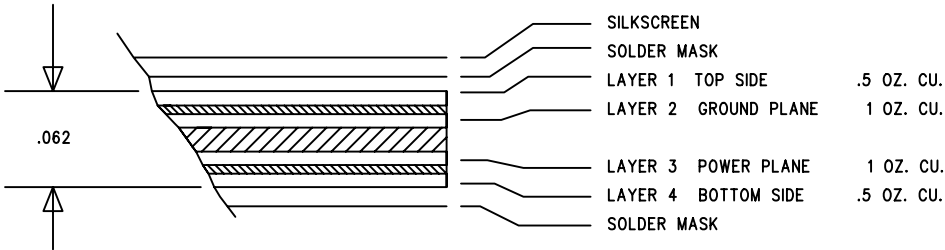
REVISIONS				
REV	DESCRIPTION OF CHANGE	DRFTR	DATE	APPROVED
1.0	NEW RELEASE	FREEDOM CAD	07/17/03	
2.0	REFER TO ECO2005-149	EASTERN CIRCUITS	01/06/06	




UNLESS OTHERWISE SPECIFIED

- BOARD TO BE FABRICATED PER IPC-6012A, CLASS 2.
- MATERIAL: POLYCLAD FR-370HR OR EQUIVALENT. OUTER LAYERS 1/2 OZ/SQFT CU
INNER LAYERS 1 OZ/SQFT CU FINISHED .062 (.055 MIN .070 MAX SEE DETAIL).
VENDOR MAY ADJUST THICKNESS TO FULFILL NOTE 9.
- PLATING: ADDITIONAL CU PLATING 1 OZ/SQFT
ALL HOLES PLATED THROUGH, EXCEPT AS NOTED IN HOLE LEGEND.
MINIMUM PTH BARREL THICKNESS = 0.0008" MINIMUM AVERAGE
PER IPC-6012A WITH AMENDMENT 1, CLASS 2 REQUIREMENTS.
WITH NO SINGLE MEASUREMENT LESS THAN 0.00071 IN THE PLATED HOLES.
- FINISH: SURFACES TO BE COATED BY ENIG OF 2 TO 10 MICROINCHES OVER
A MINIMUM OF 60-200 MICROINCHES OF LOW STRESS NICKEL.
- SOLDERMASK: SOLDER MASK TO BE TRANSPARENT GREEN LPI BOTH SIDES PER GERBER FILES.
- SILKSCREEN: WHITE EPOXY INK, APPLY TO BOTH SIDES PER GERBER FILES.
- ARTWORK: MINIMUM FEATURE SIZE = 0.005
MINIMUM AIR GAP = 0.005
- ALL DIMENSIONS ARE IN INCHES.
- CONTROLLED IMPEDANCE: BOARD TO BE BUILT TO YIELD CONTROLLED IMPEDANCE
OF 50 OHMS - 70 OHMS ON ALL .005" LINE WIDTHS
- DO NOT ADD NON-FUNCTIONAL COPPER THIEVING ON OPEN AREAS OF OUTER LAYERS.
THIEVING IS ALLOWED ON INNER LAYERS, UNLESS OTHERWISE SPECIFIED.
- VIAS SHOULD BE COVERED ACCORDING TO ONE OF THE FOLLOWING METHODS.
METHOD 1 IS PREFERRED.
METHOD 1: VIAS MUST BE FILLED WITH SOLDERMASK MATERIAL AFTER
ELECTROLESS NICKEL/IMMERSION GOLD AND BEFORE PRIMARY LPI MASK. AFTER THE
FILL IS CURED, THE PRIMARY MASK IS THEN APPLIED WITH NO VIA APERTURES
BOTH SIDES.
METHOD 2: AFTER APPLICATION OF FULL BODY ELECTROLESS NICKEL/IMMERSION
GOLD, APPLY PRIMARY MASK WITH REDUCED VIA APERTURES THAT ARE 6 MILS
LARGER THAN DRILLED HOLE DIAMETER BOTH SIDES. THEN APPLY SOLDER MASK
PLUG ON COMPONENT SIDE.
- VIA HOLES (.012) REQUIRE TANGENCY ONLY, INSTEAD OF ANNULAR RING.
- TEARDROP PADS ARE ACCEPTABLE WHERE NEEDED.
- USE IPC-D-356A NETLIST AS SUPPLIED FOR CHECKING.
- BUILD AS 1-UP PCB.
- REMOVAL OF NON-FUNCTIONAL PADS ON LAYERS 2 AND 3 IS ACCEPTABLE.

SIZE	QTY	SYM	PLATED	TOL
12	131	+	YES	+0.003-0.012
37	961	□	YES	+/-0.003
40	56	◇	YES	+/-0.003
47	3	⊗	NO	+/-0.003
53	3	⊠	NO	+0.003-0.000
60	1	+ ^A	YES	+/-0.003
67	4	+ ^B	YES	+/-0.003
76	4	+ ^C	YES	+/-0.003
125	3	+ ^D	YES	+/-0.005
128	2	+ ^E	NO	+/-0.005



4 LAYER CONSTRUCTION VIEW
SCALE : NONE

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE : .XX +/- .010 .XXX +/- .005	CONTRACT NO.		 20 Cotton Road Nashua, New Hampshire 03063 1-800-ANALOG		
	APPROVALS	DATE			
MATERIAL SEE NOTES	DRAWN FREEDOM CAD	01/06/06	FABRICATION DRAWING ADSP-21262 EZ-EXTENDER 1		
FINISH SEE NOTES	ENGINEERING				
DO NOT SCALE THIS DRAWING	QUALITY		SIZE D	DWG. NO. A0182-2003	REV. 2.0
	MANUFACTURING		SCALE 1:1		SHEET 1 OF 1