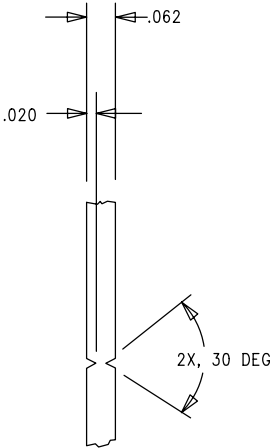
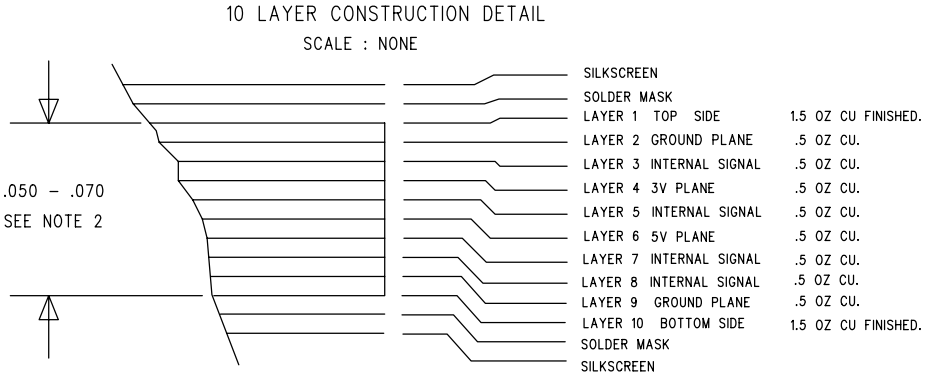



UNLESS OTHERWISE SPECIFIED

1. BOARD TO BE FABRICATED PER IPC-6012A, CLASS 2.
2. MATERIAL: POLYCLAD FR-370HR OR EQUIVALENT. OUTER LAYERS 1/2 OZ/SQFT CU
INNER LAYERS 1/2 OZ/SQFT CU FINISHED .062 (.056 MIN .076 MAX SEE DETAIL).
VENDOR MAY ADJUST THICKNESS TO FULFILL NOTE 9.
3. PLATING: ADDITIONAL CU PLATING 1 OZ/SQFT
ALL HOLES PLATED THROUGH, EXCEPT AS NOTED IN HOLE LEGEND.
MINIMUM PTH BARREL THICKNESS = 0.0008" MINIMUM AVERAGE
PER IPC-6012A WITH AMENDMENT 1, CLASS 2 REQUIREMENTS.
WITH NO SINGLE MEASUREMENT LESS THAN 0.00071 IN THE PLATED HOLES.
4. FINISH: SURFACES TO BE COATED BY ENIG OF 2 TO 10 MICROINCHES OVER
A MINIMUM OF 60-200 MICROINCHES OF LOW STRESS NICKEL.
5. SOLDERMASK: SOLDER MASK TO BE TRANSPARENT GREEN LPI BOTH SIDES PER GERBER FILES.
6. SILKSCREEN: WHITE EPOXY INK, APPLY TO BOTH SIDES PER GERBER FILES.
7. ARTWORK: MINIMUM FEATURE SIZE = 0.0035
MINIMUM AIR GAP = 0.003
8. ALL DIMENSIONS ARE IN INCHES.
9. CONTROLLED IMPEDANCE: BOARD TO BE BUILT TO YIELD CONTROLLED IMPEDANCE
OF 60 OHMS +/-10% ON ALL .005" LINE WIDTHS
10. DO NOT ADD NON-FUNCTIONAL COPPER THIEVING ON OPEN AREAS OF OUTER LAYERS.
THIEVING IS ALLOWED ON INNER LAYERS, UNLESS OTHERWISE SPECIFIED.
11. VIAS SHOULD BE COVERED ACCORDING TO ONE OF THE FOLLOWING METHODS.
METHOD 1 IS PREFERRED.
METHOD 1: VIAS MUST BE FILLED WITH SOLDERMASK MATERIAL AFTER
ELECTROLESS NICKEL/IMMERSION GOLD AND BEFORE PRIMARY LPI MASK. AFTER THE
FILL IS CURED, THE PRIMARY MASK IS THEN APPLIED WITH NO VIA APERTURES BOTH SIDES.
METHOD 2: AFTER APPLICATION OF FULL BODY ELECTROLESS NICKEL/IMMERSION
GOLD, APPLY PRIMARY MASK WITH REDUCED VIA APERTURES THAT ARE 6 MILS
LARGER THAN DRILLED HOLE DIAMETER BOTH SIDES. THEN APPLY SOLDER MASK PLUG ON COMPONENT SIDE.
12. VIA HOLES (.006, .0079, .010, & .012) REQUIRE TANGENCY ONLY, INSTEAD OF ANNULAR RING.
13. TEARDROP PADS ARE ACCEPTABLE WHERE NEEDED.
14. REMOVAL OF NON-FUNCTIONAL PADS ON LAYERS 3, 5, 7, AND 8 IS ACCEPTABLE.
15. USE IPC-D-356A NETLIST AS SUPPLIED FOR CHECKING.
16. V SCORE 3 LINES ON BOTH SIDES OF THE BOARD. SEE DETAIL.
17. BUILD AS 1-UP PCB.

SIZE	QTY	SYM	PLATED	TOL
7.9	154	✕	YES	+0.003-0.0079
10	2	□	YES	+0.003-0.010
12	1760	◇	YES	+0.003-0.012
37	63	⊗	YES	+/-0.003
41	31	⊗	YES	+/-0.003
51	16	+ ^A	YES	+/-0.003
59	8	+ ^B	YES	+/-0.003
125	11	+ ^D	YES	+/-0.005
53	6	+ ^E	NO	+0.003-0.000
125	6	+ ^F	NO	+/-0.005
140	6	+ ^G	NO	+/-0.005



SCORING DETAIL

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE : .XX +/- .010 .XXX +/- .005	CONTRACT NO.		 20 Cotton Road Nashua, New Hampshire 03063 (603) 883-2430 FAX (603) 882-2655	
	APPROVALS	DATE		
MATERIAL SEE NOTES	DRAWN	07/09/07	FABRICATION DRAWING ADSP-BF561 EZ-KIT LITE	
	CHECKED			
FINISH SEE NOTES	ENGINEERING		SIZE B	DWG. NO. A0185-2003-REF
	QUALITY			REV. 2.1
DO NOT SCALE THIS DRAWING		MANUFACTURING	SCALE 1 : 1	SHEET 1 OF 1