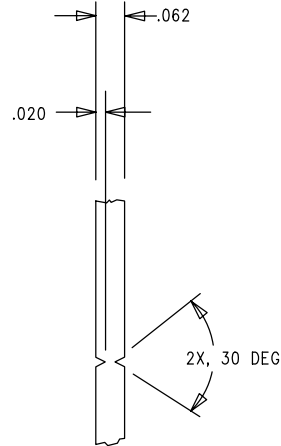
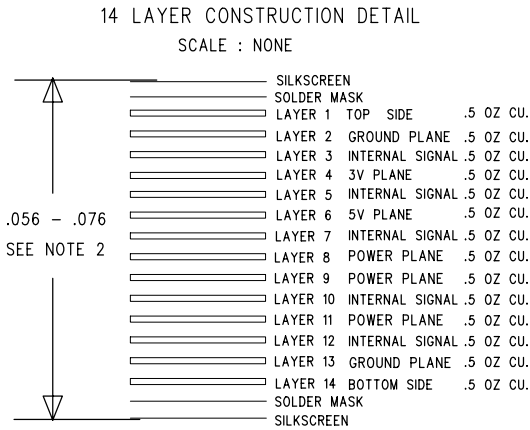


UNLESS OTHERWISE SPECIFIED


- BOARD TO BE FABRICATED PER IPC-6012A, CLASS 2.
- MATERIAL: POLYCLAD FR-370HR OR EQUIVALENT. OUTER LAYERS 1/2 OZ/SQFT CU  
INNER LAYERS 1/2 OZ/SQFT CU FINISHED .062 (.056 MIN .076 MAX SEE DETAIL).  
VENDOR MAY ADJUST THICKNESS TO FULFILL NOTE 9.
- PLATING: ADDITIONAL CU PLATING 1 OZ/SQFT  
ALL HOLES PLATED THROUGH, EXCEPT AS NOTED IN HOLE LEGEND.  
MINIMUM PTH BARREL THICKNESS = 0.0008" MINIMUM AVERAGE  
PER IPC-6012A WITH AMENDMENT 1, CLASS 2 REQUIREMENTS.  
WITH NO SINGLE MEASUREMENT LESS THAN 0.00071 IN THE PLATED HOLES.
- FINISH: SURFACES TO BE COATED BY ENIG OF 2 TO 10 MICROINCHES OVER  
A MINIMUM OF 60-200 MICROINCHES OF LOW STRESS NICKEL.
- SOLDERMASK: SOLDER MASK TO BE TRANSPARENT GREEN LPI BOTH SIDES PER GERBER FILES.
- SILKSCREEN: WHITE EPOXY INK, APPLY TO BOTH SIDES PER GERBER FILES.
- ARTWORK: MINIMUM FEATURE SIZE = 0.00349  
MINIMUM AIR GAP = 0.00450
- ALL DIMENSIONS ARE IN INCHES.
- CONTROLLED IMPEDANCE: BOARD TO BE BUILT TO YIELD CONTROLLED IMPEDANCE  
OF 55 OHMS +/- 10% ON ALL .00350" & .00492" SINGLE-ENDED TRACE WIDTHS  
OF 100 OHMS +/- 10% ON ALL .00349" DIFFERENTIAL PAIR TRACE WIDTHS
- DO NOT ADD NON-FUNCTIONAL COPPER THIEVING ON OPEN AREAS OF OUTER LAYERS.  
THIEVING IS ALLOWED ON INNER LAYERS, UNLESS OTHERWISE SPECIFIED.
- VIAS SHOULD BE COVERED ACCORDING TO ONE OF THE FOLLOWING METHODS.  
METHOD 1 IS PREFERRED.  
METHOD 1: VIAS MUST BE FILLED WITH SOLDERMASK MATERIAL AFTER  
ELECTROLESS NICKEL/IMMERSION GOLD AND BEFORE PRIMARY LPI MASK. AFTER THE  
FILL IS CURED, THE PRIMARY MASK IS THEN APPLIED WITH NO VIA APERTURES BOTH SIDES.  
METHOD 2: AFTER APPLICATION OF FULL BODY ELECTROLESS NICKEL/IMMERSION  
GOLD, APPLY PRIMARY MASK WITH REDUCED VIA APERTURES THAT ARE 6 MILS  
LARGER THAN DRILLED HOLE DIAMETER BOTH SIDES. THEN APPLY SOLDER MASK PLUG ON COMPONENT SIDE.
- VIA HOLES (.006,.010, & .014) REQUIRE TANGENCY ONLY, INSTEAD OF ANNULAR RING.
- TEARDROP PADS ARE ACCEPTABLE WHERE NEEDED.
- USE IPC-D-356A NETLIST AS SUPPLIED FOR CHECKING.  
7 SHORTS ARE PRESENT, SEE READ ME FILE.
- V SCORE 3 LINES ON BOTH SIDES OF THE BOARD. SEE DETAIL.
- BUILD AS 1-UP PCB.



SIZE	QTY	SYM	PLATED	TOL
6	25	+	YES	+0.003-0.006
10	2413	×	YES	+0.003-0.010
14	505	□	YES	+0.003-0.014
28	11	◇	YES	+/-0.003
37	90	⊗	YES	+/-0.003
41	30	⊗	YES	+/-0.003
70	10	⊕	YES	+/-0.003
125	8	⊕	YES	+/-0.005
53	6	⊕	NO	+0.003-0.000
67	6	⊕	NO	+/-0.003
128	12	⊕	NO	+/-0.005



SCORING DETAIL

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE : .XX +/- .010 .XXX +/- .005		CONTRACT NO.		<div><div></div><div>ANALOG DEVICES</div></div> <div>20 Cotton Road Nashua, New Hampshire 03063 (603) 883-2430 FAX (603) 882-2655</div>		
		APPROVALS	DATE			
MATERIAL	SEE NOTES	DRAWN	06/28/07	FABRICATION DRAWING ADSP-TS201S EZ-KIT LITE		
		CHECKED				
FINISH	SEE NOTES	ENGINEERING		SIZE B	DWG. NO. A0178-2002-REF	REV. 2.1
		QUALITY				
DO NOT SCALE THIS DRAWING	MANUFACTURING			SCALE 1 : 1	SHEET 1 OF 1	