

# **Blackfin FPGA EZ-Extender**

## **Revision 1.0**

### **Reference Design Information**

#### **Introduction:**

This design package contains the electronic information for the design, layout, fabrication and assembly of the EZ-Extender.

This information is provided for reference only. Analog Devices is not responsible for any errors that may be contained in the information. There is no support provided for any of this information and no additional information will be provided.

This information is only provided in the formats described below and not available in any other formats.

#### **Package Contents:**

##### **Schematics**

- Complete schematic project created with Mentor Graphics DxDesigner, version 2007

##### **Layout**

- PCB layout file created from PADS Layout, version 2007
- PDF file containing all the board layers, fabrication and assembly drawing

##### **Fabrication**

- Contains all the Gerber and N/C drill information needed to fabricate the board

##### **Assembly**

- Contains all the standard files needed to assembly the board