



# **Release Notes for CrossCore Embedded Studio 2.9.2**

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## 2 Introduction

This document describes the changes for [CrossCore Embedded Studio](#) (CCES) 2.9.2. You can find the release notes for older releases in the docs sub-directory of your CCES installation as well as an Installation Guide which will help you install this release.

## 3 New and Noteworthy

### 3.1 Device Programmer UI

The IDE now provides a graphical user interface for the command-line device programmer (`c\ldp`) utility. Documentation for this utility can be found in the Online Help in the section *Device Programming using the Integrated Development Environment*, via:

*Integrated Development Environment > Working with Bootable and Non-Bootable Files > Device Programmer*

## 4 Changes That Might Impact Backwards Compatibility

### 4.1 SSLDD 3.0 Support for ADSP-SC5xx (1.0.0) Addin

The "SSLDD 3.0 Support for ADSP-SC5xx (1.0.0)" add-in is no longer be available. Instead of this add-in, new individual add-ins for each module are provided to add the SSLDD 3.0 support for SC5xx. The existing projects which are using the "SSLDD 3.0 Support for ADSP-SC5xx (1.0.0)" add-in, will show "add-in not available" error. For these projects, it is recommended to un-install the existing "SSLDD 3.0 Support for ADSP-SC5xx (1.0.0)" add-in and install the newly available "xxx driver support for SC5xx (1.0.0)" add-ins as per the application's requirement.

### 4.2 HADC and TMU support removed for ADSP-2156x processors

Housekeeping ADC (HADC) and Thermal Monitoring Unit (TMU) are not supported for ADSP-2156x family processors.

### 4.3 Changes to Compiler Anomaly Workarounds

#### 4.3.1 19000058

The compiler workaround for anomaly 19000058, which affects ADSP-BF7xx parts, has been modified so that it correctly works around the case where an MMR access occurs immediately after returning from a function call. More information may be found at CrossCore® Embedded Studio 2.9.2 > Integrated Development Environment > Tools Support for IC Anomalies > IC Anomalies by Processor.

#### 4.3.2 20000083

The 20000083 anomaly, which affects ADSP-2157x, ADSP-2158x, ADSP-SC57x and ADSP-SC58x parts, may occur when the last instruction in the anomalous assembly sequence is a pre-modify write to memory, as well as when it is a pre-modify read from memory. Correspondingly, the compiler now works around this case where necessary. More information may be found at CrossCore® Embedded Studio 2.9.2 > Integrated Development Environment > Tools Support for IC Anomalies > IC Anomalies by Processor.