Release Notes for ADSP-CM41x EZ-Kit Lite® Board Support Package 1.0.0 For Keil MDK
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Thank you for installing the ADSP-CM41x EZ-Kit Lite® Board Support Package (BSP). The BSP provides software and documentation in support of the ADSP-CM41x EZ-Kit Lite.

The Board Support Package is designed to work with Keil MDK embedded software development tools. For more details on Keil, please visit http://www.keil.com The ADSP-CM41x EZ-Kit Lite BSP provides comprehensive software support for the ADSP-CM41x EZ-Kit Lite which includes drivers and services. The BSP also provides comprehensive examples which demonstrate the on-chip drivers and services. The documentation for the device drivers and services is included in the BSP.
1 Release Dependencies

- Requires Keil MDK version 5.17 or later.
- Requires Segger J-Link USB drivers version 5.10p.
2 Release Testing

The BSP has been tested with the ADSP-CM419F EZ-KIT Lite BOM 1.2
3 License Checking

The BSP software does not perform any license checking. Use of the BSP software is subject to the Software License Agreement presented during installation.
4 Installation Steps

This BSP can be obtained and installed in two ways:

1. From www.analog.com. This requires a separate installation step.
2. Directly within the Keil MDK uVision tools.

4.1 From www.analog.com

The BSP consists of two separate PACK files, one for each ARM core on the ADSP-CM41x processor. These two PACK files can be obtained from www.analog.com, as a zip file.

To complete the installation, perform the following steps:

1. Extract the zip file contents into a temporary directory of your choice.
2. Open the Keil MDK uVision IDE.
3. Invoke the Keil Pack Installer.
4. Import the ADSP-CM41x-M0 Pack:
   a. Do File > Import
   b. In the Import Packs pop-up dialog, browse to the directory specified during installation.
   c. Select the AnalogDevices.CM41x_M0_DFP.1.0.0.pack file.
   d. Click Open.
   e. After importing is complete, Analog Devices ARM Cortex-M0 CM41x Family Device Support will be listed in the Packs tab, as Offline (as the pack was installed from a file, not downloaded directly).
5. Repeat these steps for the AnalogDevices.CM41x_M4_DFP.1.0.0.pack file.
6. Once both PACK files are installed, you may remove the temporary directory.

4.2 Directly within the Keil MDK uVision tools.

PACK files for Analog Devices Cortex-M-based processors may be obtained directly from within the Keil MDK uVision tools.

1. Launch the Keil uVision IDE.
2. Click on the Pack Installer icon.
3. In the Pack Installer, select Packs > Check for Updates.

4. In the Device tab, select AnalogDevices, then CM41x Mixed-Signal Processors.

5. In the Packs tab, under Device Specific, select AnalogDevices::CM41x_M0_DFP, and click on Install.

6. In the Packs tab, under Device Specific, select AnalogDevices::CM41x_M4_DFP, and click on Install.
5 Release Content

This release contains the following sets of components:

- Source files for device drivers and services. These components are authored by Analog Devices, for use on the ADSP-CM41x processor.
- Examples for device drivers and services. These components are authored by Analog Devices, and demonstrate the use of the device drivers and services.
- Toolchain support. These components are authored by Analog Devices, and are installed into the toolchain to configure it to recognize the ADSP-CM41x processor family.
- Additional utilities. These components are authored by Analog Devices, and assist in the generation of applications for the ADSP-CM41x processor family.
- Documentation
6 Source files for drivers and services

<table>
<thead>
<tr>
<th>Source files and include files</th>
</tr>
</thead>
<tbody>
<tr>
<td>adi_int, adi_nvic</td>
</tr>
<tr>
<td>adi_types.h</td>
</tr>
<tr>
<td><em>ADSP-CM41x</em>.h</td>
</tr>
<tr>
<td>System</td>
</tr>
<tr>
<td>Startup</td>
</tr>
</tbody>
</table>

Base typedefs
Device descriptions and macro files
Source files and include files
Source files and include files

Various peripheral device driver sources and include files in the “src” and “inc” directories.
7 Toolchain Support Files

The following common system infrastructure framework files are installed in the toolchain and should be used in all projects.

7.1 Keil MDK Tool Chain Configuration files

<table>
<thead>
<tr>
<th>CM41x_M0.svd, CM41x_M4.svd</th>
<th>Debugger register display</th>
</tr>
</thead>
<tbody>
<tr>
<td>CM41x_FlashA_512.FLM</td>
<td>Flash loader files</td>
</tr>
<tr>
<td>CM41x_FlashB_512.FLM</td>
<td></td>
</tr>
</tbody>
</table>
8 Additional Utilities

This utility is installed into the tools subdirectory

<table>
<thead>
<tr>
<th>PinMuxUI</th>
<th>Graphical pin mux configuration and code generation utility</th>
</tr>
</thead>
</table>


## 9 Documentation

<table>
<thead>
<tr>
<th>File Name</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>ADSP-CM41x_Software_for_MDK_Keil_ReleaseNotes.pdf (this file)</td>
<td>Release Notes</td>
</tr>
<tr>
<td>html/index.html</td>
<td>Index file for HTML-based Device Driver API documentation.</td>
</tr>
<tr>
<td>CM41x_Device_Drivers_User_Guide.pdf</td>
<td>Provides the guidelines for using the Analog Devices device drivers</td>
</tr>
<tr>
<td>ADSP-CM41x_EZ-Kit_Lite_BSP_UsersGuide.pdf</td>
<td>Provides the details about ADSP-CM41x EZ-KIT Lite® Board Support Package (BSP) and its use</td>
</tr>
</tbody>
</table>
10 Examples

ADSP-CM41x processors are dual-core processors, with both a Cortex-M4 core and a Cortex-M0 core. The majority of examples are single-core examples, configured to be built for one core or the other; several of the examples are supplied in two configurations, one for each core. The “mbox” examples are dual-core examples: there is a project for each core, and the complete example consists of both projects, running simultaneously on different cores of the same processor.

Please note that examples which runs the Cortex-M0 core of the ADSP-CM41x processor will output all debug information to UART0 which is connected to J9 (RS232 Terminal). Please refer to the readme and ADSP-CM41x_EZ-Kit_Lite_BSP_UsersGuide.pdf for more details.

10.1 ProgramInfoSpace

This example contains several projects which demonstrate how the "info space" areas of the flash memories can be reprogrammed to specific values. This is necessary in order to lock the device to protect IP programmed into the main flash memories, and to enable CRC-checking of flash memory. The example contains the following projects:

1. An application to report the state of the info space.
2. An application to erase and unlock a part with corrupted security information, returning it to "clean", empty state.
3. An application to apply configuration data. This application can also enable flash integrity CRC checks.
## 10.2 Examples for Drivers

<table>
<thead>
<tr>
<th></th>
<th>Examples</th>
<th>Cortex</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>CRC</td>
<td>Cortex-M4</td>
<td>Uses on-chip CRC controller</td>
</tr>
<tr>
<td>2</td>
<td>UART</td>
<td>Cortex-M0</td>
<td>Demonstrates loop-back of data between Tx and Rx</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Cortex-M4</td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>SPI</td>
<td>Cortex-M4</td>
<td>Demonstrates data loop-back</td>
</tr>
<tr>
<td>4</td>
<td>TRU</td>
<td>Cortex-M0</td>
<td>Demonstrates triggers</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Cortex-M4</td>
<td></td>
</tr>
<tr>
<td>5</td>
<td>TTU</td>
<td>Cortex-M4</td>
<td>Demonstrates trigger timing</td>
</tr>
<tr>
<td>6</td>
<td>Math Unit</td>
<td>Cortex-M4</td>
<td>Demonstrates math acceleration.</td>
</tr>
<tr>
<td>8</td>
<td>ADCC</td>
<td>Cortex-M0</td>
<td>Demonstrates ADC Controller.</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Cortex-M4</td>
<td></td>
</tr>
<tr>
<td>9</td>
<td>SPORT</td>
<td>Cortex-M4</td>
<td>Demonstrate SPORT loop-back example.</td>
</tr>
</tbody>
</table>
### 10.3 Examples for Services

<table>
<thead>
<tr>
<th></th>
<th>Service</th>
<th>Cortex</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Timer</td>
<td>M0 M4</td>
<td>Demonstrates the timer service</td>
</tr>
<tr>
<td>2</td>
<td>MDMA</td>
<td>M4</td>
<td>Demonstrates the memory-to-memory DMA service</td>
</tr>
<tr>
<td>3</td>
<td>SysTick</td>
<td>M0 M4</td>
<td>Demonstrates the System Tick interrupt.</td>
</tr>
<tr>
<td>4</td>
<td>GPIO</td>
<td>M0 M4</td>
<td>Demonstrates General-purpose I/O</td>
</tr>
<tr>
<td>5</td>
<td>MBOX</td>
<td>M0 M4</td>
<td>Demonstrates the mailbox between the two cores.</td>
</tr>
<tr>
<td>6</td>
<td>SMPU</td>
<td>M0 M4</td>
<td>Demonstrates System Memory Protection Unit</td>
</tr>
</tbody>
</table>
11 Location

By default, the BSP software will be installed at the following location as standard Keil pack files:
C:\Keil_v5\ARM\Pack\AnalogDevices
12 Contacting Technical Support

Submit your questions online at:
http://www.analog.com/support

E-mail your Processors and DSP applications and processor questions to:
processor.support@analog.com OR
processor.china@analog.com (Greater China support)

For MDK Keil tool chain support please visit
http://www.keil.com/support
13 Known issues with the ADSP-CM41x EZ-Kit Lite Board Support Package

- ADUCK06-116: There is no utility provided for enabling the Cortex-M4 application to program the Cortex-M0’s application image into the appropriate SRAM before releasing the Cortex-M0 core.
- It is advisable to user to reset the board using SW6 before loading stand-alone M4 or M0 examples to prevent any error while loading.
- ADUCK06-67: There is no utility provided for computing a CRC value for supporting flash integrity checks.
- ADUCK06-118: Provide Part Specific Configurations in the ADSP-CM41x PinMux UI (see below)

13.1 ADUCK06-118

The Pin Multiplexing Tool is available to assist in building pin multiplexing applications for the ADSP-CM41x family. The 1.0.0 release provides a single generic family part ADSP-CM41x, fully configured for the 210 Ball BGA with 5 UARTs and 16 GP timers.

This matches the full configuration available with the ADSP-CM418F/ADSP-CM419F generic parts.

Some pin selections will not be valid for parts that do not support this full configuration.

Note that the ASDP-CM411F and ADSP-CM412F reduce the number of UARTs and the ADSP-CM411F, ADSP-CM412F and ADSP-CM413F reduce the number of GP timers.

Consult the Product Features table in the ADSP-CM41x Mix-Signal Control Processor datasheet which describes the features available for each of the generic parts:

- ADSP-CM411F
- ADSP-CM412F
- ADSP-CM413F
- ADSP-CM416F
- ADSP-CM417F
- ADSP-CM418F
- ADSP-CM419F