



# **Release Notes for EV-SC8xx EZ-KIT<sup>®</sup> Rel. 1.0.0**

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# 1 Release Notes

Thank you for installing the EV-SC8xx EZ-KIT® Board Support Package (BSP) Rel.1.0.0. The BSP provides software and documentation in support of the EV-SC8xx EZ-KIT®. The EV-SC8xx EZ-KIT is designed for use with CrossCore® Embedded Studio (CCES) for Analog Devices Processors software development tools. The CCES development environment aids advanced application code development and debug, such as:

- Create, compile, assemble, and link application programs written in C++, C, and assembly.
- Load, run, step, halt, and set breakpoints in application programs
- Read and write data and program memory
- Read and write core and peripheral registers
- Plot memory.

For more details on CCES, please visit [www.analog.com/cces](http://www.analog.com/cces) .

The EV-SC8xx EZ-KIT® BSP provides comprehensive software support for the EV-SC8xx EZ-KIT® ( ADSP-21835,ADSP-SC835) . In this release, various examples are provided to demonstrate the on-chip drivers and services.

## 2 Release Dependencies:

- Requires CrossCore® Embedded Studio version 3.0.0
- EV-SC835-SOM EZ-KIT® Rev A
- EV-21835-SOM EZ-KIT® Rev A
- EV-SOMCRR-EZ-KIT® Rev D

## 3 Examples:

### 3.1 Power\_On\_Self\_Test:

This example allows the user to test many peripherals of the EV-SC8xx EZ-KIT®. Readme is provided in the POST example to understand how these tests are run. Power\_On\_Self\_Test is available for SHARCFX core in both debug & release mode.

### 3.2 Drivers/Services:

The following Device Driver and System Service examples are provided for EV-SC8xx EZ-KIT® Board Support Package (BSP):

#### Device Drivers

- ASRC
- ADC-DAC
- CAN-FD
- CRC
- Crypto (PKTE)
- EMAC
- FIR
- HADC
- IIR
- SPDIF
- SPI
- SPORT
- TMU
- TWI
- UART
- xSPI

#### System Services

- DAI/DRU

- DMA (MDMA, EMDMA)
- FPLL
- GPIO
- PWR
- SWU
- TMR
- TRU
- WD

**Known Issues/Disclaimers:**

- "xSPI\_HyperFlash\_Access" example must be run on standalone EV-21835-SOM/EV-SC835-SOM board, without connecting to any EV-SOMCRR-EZKIT or EV-SOMCRR-EZLITE board.
- Cache must be disabled to run xSPI, EMDMA examples (By default it's disabled in the project settings).
- Due to the implementation of random delay , xSPI\_HyperFlash\_Access / xSPI\_NORFlash\_Access examples might take few seconds to erase the flash memory.
- Plot view is not supported in CCES-3.0.0 yet for SC8xx, so ASRC examples will not be able to show the plot view.
- SPIDMAMode example is not operational.