



ADSP-SC5xx EZ-KIT Lite[®] Board Support Package v2.0.2 Release Notes

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1 Release Dependencies

Requires CrossCore® Embedded Studio (CCES) version 2.6.0 or newer.

What's New

ADSP-SC58x 500MHz Core Clock Support

All the examples provided with the ADSP-SC5xx EZ-KIT Lite® Board Support Package are modified to use the default CGU0 settings done in `init_code` which is 450 MHz Core Clock and 225 MHz system clock.

The ADSP-SC5xx EZ-KIT Lite® Board Support Package now also supports 500 MHz Core Clock for the ADSP-SC589 and ADSP-SC584 processors. If you are building for a processor that is certified to run at 500MHz, and wish to set the core clock speed to 500MHz, you can generate a new preload file by simply defining the macro `CLOCK_SPEED_500MHZ` in "`sc589_preload_Core0`" project which is available in "`C:\Analog Devices\CrossCore Embedded Studio 2.7.0\SHARC\ldr\init_code\SC58x_Init\sc589_preload_Core0`". You need to build this project and then use the generated preload file while defining the "`Debug Configuration`". As the maximum clock speed for DDR2 is 400MHz, the DDR settings will not be changed and CGU1 will be used to generate the DDR clock.

Refer to CrossCore® Embedded Studio Release notes for more information.

There are a few examples for which you may need to add "`CLOCK_SPEED_500MHZ`" to their preprocessor definitions in case `CCLK` is configured as 500 MHz. Please refer to readmes provided in the examples.

Most of the examples now do not use `adi_pwr_SetFreq()` API of power service. Using this function for a new clock configuration, which requires a change in PLL, may lead to out of spec DDR clock.

Added 4 Channel and 8 channel TDM mode support in ADC_DAC_Playback example for ADSP-SC589 & ADSP-SC573

This example demonstrates the usage of the ADAU1979 ADC and ADAU1962a DAC. In playback mode, this example can now run in I2S, TDM4 and TDM8 modes. Please refer to the example readme for more information

Fixed the issue with Diagnostic_Fault example for ADSP-SC584 & ADSP-SC573

This example shows how to configure and use the `adau1977` device driver. It is now fixed for ADSP-SC584 and ADSP-SC573 processors.

2 Known issues in version 2.0.2

For the latest list of known issues please consult our [Software and Tools Anomalies Search](#) page.

- **EZKSW23-783** : Example "ASRC_Playback" won't launch in release mode for ADSP-SC589 and ADSP-SC573.
- Online help and Board support package do not have correct ADSP-SC573 EZ-Board manual. Please refer to EZKSW23-736, EZKSW23-645 and EZKSW23-799.
- Voltages measured for INA3221 test may vary with different BOM revisions of the boards. The voltages measured depends on other peripherals being left on or turned on. This may result in failures of INA3221 tests with different BOM revisions.

3 ADSP-SC58x EZ-KIT Lite® Board Support Package v2.0.1

Release Notes

3.1 Release Dependencies

Requires CrossCore® Embedded Studio (CCES) version 2.4.0 or newer.

This release was tested with:

- ADSP-SC589 EZ-KIT Rev 1.0, BOM Rev 1.6
- ADSP-SC589 EZ-KIT Rev 1.0, BOM Rev 1.4
- ADSP-SC584 EZ-KIT Rev 1.0, BOM Rev 1.4
- ADSP-SC584 EZ-KIT Rev 1.0, BOM Rev 1.1
- ADSP-SC573 EZ-KIT Rev 0.0, BOM Rev 1.6

3.2 New features and examples in version 2.0.1

3.3 Known issues in version 2.0.1

For the latest list of known issues please consult our [Software and Tools Anomalies Search](#) page.

Due to the POST example being loaded into flash on the EZ-Kit, the SD Card examples may exhibit unexpected behaviour. To avoid this behaviour it is recommended that you change the Boot Mode dial on the Ez-Kit (SW1) to Boot Mode 0 (No boot from flash) when loading applications via the emulator.

When running the Ethernet to Ethernet examples with two ADSP-SC57x EZ-Kit boards the application may encounter some packet loss.

Ethernet examples provided as part of this product require the latest versions of the ADSP-SC589, ADSP-SC584 and ADSP-SC573 EZ-Kit boards in order for the tests to pass.

As of release these versions are:

- ADSP-SC589 - Rev 1.0, BOM 1.6
- ADSP-SC584 - Rev 1.0, BOM 1.4
- ADSP-SC573 - Rev 0.0, BOM 1.6

4 ADSP-SC58x EZ-KIT Lite® Board Support Package v2.0.0

Release Notes

This release note includes the release notes for previous updates, with some corrections where necessary. Release notes for previous updates can be found at the end of this document.

4.1 Release Dependencies

Requires CrossCore® Embedded Studio (CCES) version 2.3.0 or newer.

This release was tested with:

- ADSP-SC589 EZ-KIT Rev 1.0, BOM Rev 1.6
- ADSP-SC589 EZ-KIT Rev 1.0, BOM Rev 1.4
- ADSP-SC584 EZ-KIT Rev 1.0, BOM Rev 1.4
- ADSP-SC584 EZ-KIT Rev 1.0, BOM Rev 1.1
- ADSP-SC573 EZ-KIT Rev 0.0, BOM Rev 1.2

4.2 New features and examples in version 2.0.0

4.2.1 New SHARC processors

This release adds support for the ADSP-SC57x/ADSP-2157x family of processors.

4.2.2 New flashable ThreeCore example

A new example has been added to this release that demonstrates how a simple three-core application can be built and flashed to an ADSP-SC5xx EZ-Kit. This example is called "Three Core Example" and interacts with the user through Push Button presses and blinking LEDs. The ARM Cortex-A5 core and the two SHARC+ cores communicate using the MCAPI protocol. Printed output can be directed to the console (if debugging via the emulator), over UART, or disabled altogether. The readme for this example explains how to flash the loader image for the three executables to the EZ-Kit.

You can find this new example using the Examples Browser, by searching for "Three Core" or MCAPI.

4.2.3 New FFT Accelerator examples

Three new examples are provided that show how the FFT accelerator on ADSP-SC58x processors can be used with a DMA descriptor chain to obtain maximum throughput from the accelerator while leaving the core free for other work. One of these examples also shows how the FFT accelerator can be used for perform a block FIR filter. These examples are:

- Small FFT Descriptor Chain
- Large FFT Non-blocking
- FFT FIR Non-blocking

These examples can be found by searching for FFT in the Keywords field of the Examples Browser.

4.3 Known issues in version 2.0.0

For the latest list of known issues please consult our [Software and Tools Anomalies Search](#) page.

4.4 Upgrading from an earlier of the ADSP-SC58x EZ-Kit Lite Board Support Package

Due to the renaming of this product from ADSP-SC58x to ADSP-SC5xx, automatic upgrading of the product is not available.

Please uninstall any previous version of the product before installing the new product in its place.

5 ADSP-SC58x EZ-KIT Lite® Board Support Package v1.1.0

Release Notes

5.1 Release Dependencies

Requires CrossCore® Embedded Studio (CCES) version 2.0.0 or newer. For silicon revision 1.0 of ADSP-SC584 and ADSP-SC589 support it is recommended to use CCES version 2.2.0 or newer.

This release was tested with:

- ADSP-SC589 EZ-KIT Rev 1.0, BOM Rev 1.6
- ADSP-SC589 EZ-KIT Rev 1.0, BOM Rev 1.4
- ADSP-SC584 EZ-KIT Rev 1.0, BOM Rev 1.4
- ADSP-SC584 EZ-KIT Rev 1.0, BOM Rev 1.1

5.2 New features and examples in version 1.1.0

There are no new features or examples.

5.3 Changes in version 1.1.0

- Validated support of silicon revision 1.0 of ADSP-SC584 and ADSP-SC589. Examples are by default configured to build for silicon revision 'any'.
- Generated files, such as those created by the Startup Code/LDF add-in, are no longer included in BSP example projects. Instead the required files are generated when the example is opened via the example-browser in CCES. When this occurs, an Informational message will be seen in the Problems View in the IDDE.
- Example projects have been modified to add `-absolute-path-dependencies` to the additional options for assembler and compiler settings. This is to avoid possible problems when a deeply-nested directory structure can cause the Windows path length limit to be exceeded.

5.4 Known issues in version 1.1.0

For the latest list of known issues please consult our [Software and Tools Anomalies Search](#) page.

6 ADSP-SC58x EZ-KIT Lite® Board Support Package v1.0.0

Release Notes

Thank you for installing the ADSP-SC58x EZ-KIT Lite® Support Package (BSP). The BSP provides software and documentation in support of the ADSP-SC58x EZ-KIT Lite®.

The EZ-Kit Lite is designed for use with CrossCore® Embedded Studio (CCES) for Analog Devices Processors software development tools. The CCES development environment aids advanced application code development and debug, such as:

- Create, compile, assemble, and link application programs written in C++, C, and assembly
- Load, run, step, halt, and set breakpoints in application programs
- Read and write data and program memory
- Read and write core and peripheral registers
- Plot memory

For more details on CCES, please visit www.analog.com/cces.

The ADSP-SC58x EZ-KIT Lite® BSP provides comprehensive software support for the ADSP-SC58x EZ-KIT Lite®. Specifically, drivers, examples and code sketches are included for the following components:

- INA3221 shunt and bus voltage monitor ADC driver
- 12-Channel, 192 kHz, 24-Bit DAC (ADAU1962A) Driver

The BSP also provides comprehensive examples which demonstrates the on-chip drivers and services.

The CCES Help environment provides complete hardware and software documentation.

6.1 Release Dependencies

Requires CrossCore® Embedded Studio version 2.0.0

6.2 Release Testing

The BSP has been tested with the ADSP-SC589 EZ-KIT version 1.0, BOM 1.4 and ADSP-SC584 EZ-KIT version 0.1 BOM 1.1.

6.3 License Checking

This section of the 1.0.0 release notes has been rephrased for 1.1.0, to clarify its meaning.

The BSP software does not perform any license checking. Use of the BSP software is subject to the Software License Agreement presented during the installation.

6.4 Installation Logging

The installer does not create a log file by default. If you encounter installation issues, you can generate an installation log file by running the installer from the command prompt.

Change to the directory containing downloaded installer executable and run the following from the command prompt:

```
ADI_ADSP-SC58x_EZ-KIT-Rel1.0.0.exe /v"/l*v c:\temp\installer.log"
```

6.5 Software Requirements

To build the example projects included in the ADSP-SC58x EZ-KIT Lite® BSP, CrossCore® Embedded Studio version 2.0.0 or later is required.

6.6 Getting Started

6.6.1 Adding a Driver to a Project

When adding an ADSP-SC58x Driver to your project, the IDE will add the sources for the driver to the CCES Project folders, starting at "system". There will be a folder specific to the driver(s) or service(s) you have added under this folder.

6.6.2 Creating a project which includes a ADSP-SC58x driver/service

In order to create a project you should follow the instructions provided in the CrossCore® Embedded Studio help. As part of the project creation, the page "Add-in selection" contains a list of all the available add-ins for the project that you are creating based on the installed products and the project's chosen processor and type. You can see the drivers in support of the ADSP-SC58x EZ-KIT™ under the "Device Drivers and System Services" category. Within this category you will see "ADSP-SC58x EZ-KIT" which contains the drivers for the on-board peripherals (INA230 and INA3221). The on-chip peripheral drivers will be listed in "On-chip peripheral drivers" folder and the system services are listed in the "System Services" folder.

The ADSP-SC58x EZ-KIT Lite® add-in generates a call to `adi_initComponents()`. For more information on `adi_initComponents()`, please refer to the CCES help

6.6.3 Adding a ADSP-SC58x driver to an existing project

Every CrossCore® Embedded Studio project contains a System Configuration file called `system.svc` which is located in the root of the project. The file is the IDE's interface for managing the various pre-written software components used in the "system" implemented by a project. Double-clicking any `system.svc` file in a navigation view opens that file in the System Configuration Utility which allows you to see the add-ins that you currently have in your project. Click on "Add..." and select the ADSP-SC58x EZ-KIT Drivers add-in which is under the "Device Drivers and System Services" for the on-board INA230 and INA3221 ADC drivers. For adding on-chip peripherals drivers select the "On-chip peripheral drivers" and for the system services select the "System Services".

Notes:

- If the IDE detects that `adi_initComponents()` is not yet present in `main()`, it prompts you to add it and offers to insert it for you.

6.6.4 Configuration

There are no ADSP-SC58x EZ-KIT Lite® driver configuration options available in the IDE.

6.6.5 Interrupts

CrossCore Embedded Studio provides a coherent interrupt management mechanism which allows for the same interface to be used in RTOS and non-RTOS applications. This means that interrupt service routines in all applications must be written in C and use the `adi_int` interface. Any thread-safety requirements or interactions with tasks are handled by the `adi_int` interface. For more information on the `adi_int` API, in CrossCore Embedded Studio go to Help > Search and enter `adi_int`.

Examples of the usage of this interrupt management mechanism are the System Services and Device Drivers provided with Crosscore Embedded Studio. By using the `adi_int` interface, the same services and drivers can be used in all applications regardless of whether an operating system is used.

6.7 Code Sketches and Examples

6.7.1 Code Sketches

CrossCore® Embedded Studio provides a mechanism by which small code fragments, called code sketches, can be generated with parameterized input provided by the user. The resulting code can then be copied and pasted to a project. Sketches for the on-board peripherals on the ADSP-SC58x EZ-KIT® are provided in the BSP. To locate the sketches specific to the ADSP-SC58x EZ-KIT® BSP, open up the example browser (Help -> Browse Examples) and then select ADSP-SC58x EZ-KIT Lite product in the "Product:" pulldown. The sketches for the on-chip drivers and system services can be located by selecting the CrossCore® Embedded Studio product in the "Product" pulldown.

6.7.2 Examples

Power_On_Self_Test:

This example allows the user to test the many peripherals of the EZ-KIT. This example is also pre-programmed into the on-board flash memory. By following the directions in the readme.html you can also program this example (or one of your own) into the EZ-KIT flash. This POST was designed so that you can use the EZ-KIT push buttons to select a specific test to run.

Device_Programmer

This example allows the user to program the flash device on the ADSP-SC58x EZ-KIT Lite® in conjunction with the Device Programmer Command-line tool. This code only serves as an example of how to program the flash and may not be optimized for the fastest programming speed. A pre-built binary exists so that users can just program the flash device without having to build the example.

Examples for Drivers:

1. ADC Example which uses the ADAU1979 ADC and the ADAU1962a DAC to operate either a four channel audio playback or a four channel analog loopback
2. INA3221 example to read the 3.3 V source, VDD_INT and VDD_EXT bus and shunt voltage on the ADSP-SC58x EZ-KIT
3. ASRC Example which uses the ASRC, PCG, ADAU1979 ADC and the ADAU1962a DAC to operate either a four channel audio playback or a four channel analog loopback
4. CRC examples which use the on-board CRC controller

5. FIR example which demonstrates how to configure the FIR device driver in floating point single rate mode
6. HADC example which uses the on-chip HADC controller
7. HAE examples which uses the on-chip HAE controller
8. IIR example which demonstrates how to configure the IIR device driver in channel interrupt mode
9. Linkport loopback example which uses the linkport driver
10. SINC example which demonstrates how to configure the SINC filter device driver
11. SPDIF examples which uses the SPDIF interface
12. SPI_flash_read example which uses SPI driver to access the on-board serial flash memory
13. SPI example which tests the 1K-bit SPI Serial EEPROM (Microchip 25LC010A) memory device.
14. TMU examples which use the on-board Thermal Monitoring Unit
15. TWI example which demonstrates how to configure the Silicon Labs Si5356A clock generator output
16. UART examples which demonstrate auto-baud and character echo using UART

Examples for Services:

1. Timer_Callback example which demonstrates the General Purpose timer service
2. Watchdog example which demonstrates the Watchdog service
3. EachDayAlarm example which uses the Real Time Clock service to generate an alarm each day
4. SetGetDateTime example which uses the Real Time Clock service to set and get the time
5. Power service example which demonstrates how to use the Power Service to change the processor core clock and system clock frequencies
6. Trigger Routing Unit (TRU) example which demonstrates the TRU functionality using Memory DMA (MDMA)
7. PWM example which demonstrates the use of the PWM driver to output a signal capable of driving a Hitec servo motor. This example requires driver sources that are not in CrossCore® Embedded Studio 2.0.0 M6 but will be in the production release of CrossCore® Embedded Studio 2.0.0

6.7.3 Location

In order to locate the ADSP-SC58x BSP examples and sketches, open CrossCore® Embedded Studio's Example Browser which can be found in CrossCore® Embedded Studio under Help. Select in the Product section "ADSP-SC58x EZ-KIT v1.0.0" for a full list of examples and sketches.

6.8 Documentation

API documentation for the ADSP-SC58x on-chip drivers and services for both SHARC+ and Cortex-A5 can be found in the CCES Help. CrossCore® Embedded Studio 2.0.0 > System Runtime Documentation > System Services > ADSP-SC58x API Reference and CrossCore® Embedded Studio 2.0.0 > System Runtime Documentation > System Services > ADSP-SC58x Cortex API Reference. General information on the driver model can be found in CCES help under CrossCore® Embedded Studio 2.0.0 > System Runtime Documentation > Device Drivers Users Guide. API documentation for the off-chip drivers (controllers populated on the EZ-KIT, ADAU1962A and INA231) in a future version of the BSP will be found under

ADSP-SC58x Board Support Package 1.0.0 > ADSP-SC58x EZ-KIT Lite® API Reference

6.9 MISRA-C Support

MISRA C is a software development standard for the C programming language developed by the Motor Industry Software Reliability Association (MISRA). Its aims are to facilitate code safety, portability, and reliability in the context of embedded systems, specifically those systems programmed in ANSI C. The compiler detects violations of the MISRA rules at compile-time, link-time, and run-time.

6.10 System Services and Device Driver Thread Safety

All system services and device drivers (SSDD) use mutexes and semaphores to ensure thread-safety. If an RTOS is present then the SSDD will use the RTOS mutex and semaphores. If an RTOS is not present then the SSDD will use a non-RTOS implementation of mutexes and semaphores (spin locks).

6.11 Contacting Technical Support

Please contact processor.support@analog.com.

6.12 Known issues with the ADSP-SC58x EZ-KIT® Board Support Package (BSP)

None.